What is claimed is:

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- 1. A method for reducing a random access memory (RAM) of an integrated circuit (IC) in display devices, comprising:
 - a. transforming a digital signal graphic data of three original colors (R, G, B) through data processing to a Y, Cb and Cr signal, where Y is a brightness signal and Cr and Cb are color signals;
 - b. selecting a Y: Cb: Cr sample ratio according MPEG standards and compressing and storing the sample ratio in the RAM of the driving IC; and
 - c. processing through another data processing to transform the signal stored in the RAM of the driving IC to an original-three-colors (R, G, B) signal format to output image data.
- 2. The method of claim 1, wherein the sample ratio of Y: Cb: Cr at step b is Y: Cb: Cr (4:2:0).
 - 3. The method of claim 1, wherein the sample ratio of Y: Cb: Cr at step b is Y: Cb: Cr (4:2:2).